

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/604,381	TSENG ET AL.	
Examiner		Art Unit		Page 1 of 1
Kaushikkumar Patel		2188		

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,146,581 A	09-1992	Kaneko, Yutaka	711/5
*	B	US-5,557,766 A	09-1996	Takiguchi et al.	711/5
*	C	US-5,655,099 A	08-1997	Bjerge et al.	711/5
*	D	US-5,913,924 A	06-1999	Zhou et al.	711/212
*	E	US-6,691,219 B2	02-2004	Ma et al.	711/219
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Definitions of stack, microcontroller (pages 5)
	V	The Authoritative Dictionary of IEEE standards terms (page 1093).
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.